be replaced with an IGBT to allow generation of the pacing pulse with opposite polarity of the first phase of the defibrillation pulse.

In the Claims:

Please add the following new Claims 35-67:

35. (New) A circuit that generates a defibrillation pulse in a defibrillation mode and a pacing pulse in a pacing mode, the circuit comprising:

an energy storage capacitor;

a charging circuit coupled to the energy storage capacitor, wherein the charging circuit is configured to charge the energy storage capacitor;

an energy transfer circuit coupled to the energy storage capacitor, the energy transfer circuit having a first output lead and a second output lead, wherein the energy transfer circuit is configured to selectively electrically couple the energy storage capacitor to the first and second output leads; and

a control circuit coupled to the charging circuit and the energy transfer circuit, wherein the control circuit is configured to cause the charging circuit to charge the energy storage capacitor to a predetermined level and, when the energy storage capacitor is charged, to control the energy transfer circuit to couple the energy storage capacitor to the first and second output leads of the energy transfer circuit so that the energy transfer circuit provides:

during the defibrillation mode, an external defibrillation pulse at the first and second output leads using energy stored in the energy storage capacitor, and

during the pacing mode, an external pacing pulse at the first and second output leads using energy stored in the energy storage capacitor.

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36. (New) The circuit of Claim 35 wherein the energy transfer circuit comprises four legs, a first leg of the energy transfer circuit including a first IGBT switch circuit and a second, a third and fourth leg of the energy transfer circuit each including an SCR switch circuit.

37. (New) The circuit of Claim 36 wherein the energy transfer circuit comprises a bypass circuit, the bypass circuit being coupled in parallel with the third leg of the energy transfer circuit, and wherein the bypass circuit is configured to provide a conductive path that bypasses the third leg of the energy transfer circuit during the pacing mode and is configured to open circuit the conductive path during the defibrillation mode.

38. (New) The circuit of Claim 37 wherein the defibrillation pulse is selectably provided as a biphasic pulse and the pacing pulse is provided as a monophasic pulse.

39. (New) The circuit of Claim 37 wherein, during the pacing mode, the control circuit is configured to determine the predetermined level in charging the energy storage capacitor to achieve a predetermined current level for a subsequently provided pacing pulse.

40. (New) The circuit of Claim 36 wherein the H-bridge circuit comprises a current source circuit, the current source circuit being coupled in parallel with the third leg of the energy transfer circuit, and wherein the current source circuit is configured to provide a configurable current to the first output lead during the pacing mode and is configured to provide essentially no current to the first output lead during the defibrillation mode.

41. (New) The circuit of Claim 40 wherein, during the pacing mode, the control circuit is configured to cause the current source circuit to provide the configurable current with a predetermined current level.

42. (New) The circuit of Claim 40 wherein the current source circuit comprises an IGBT and a resistor.

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43. (New) The circuit of Claim 35 wherein the energy transfer circuit comprises four legs, a first leg and a second leg of the energy transfer circuit each including an IGBT switch circuit, and a third leg and a fourth leg of the energy transfer circuit each including an SCR switch circuit.

44. (New) The circuit of Claim 43 wherein the H-bridge circuit comprises a bypass circuit, the bypass circuit being coupled in parallel with the third leg of the energy transfer circuit, and wherein the bypass circuit is configured to provide a conductive path that bypasses the third leg of the energy transfer circuit during the pacing mode and is configured to open circuit the conductive path during the defibrillation mode.

45. (New) The circuit of Claim 44 wherein the defibrillation pulse is selectably provided as a biphasic pulse having a first phase with a first polarity and a second phase of a second polarity, and wherein the pacing pulse is provided as a monophasic pulse with the second polarity.

46. (New) The circuit of Claim 45 wherein, during the pacing mode, the control circuit is configured to determine the predetermined level in charging the energy storage capacitor to achieve a predetermined current level for a subsequently provided pacing pulse.

47. (New) The circuit of Claim 43 wherein the H-bridge circuit comprises a current source circuit, the current source circuit being coupled in parallel with the third leg of the energy transfer circuit, and wherein the current source circuit is configured to provide a configurable current to the first output lead during the pacing mode and is configured to provide essentially no current to the first output lead during the defibrillation mode.

48. (New) The circuit of Claim 47 wherein, during the pacing mode, the control circuit is configured to cause the current source circuit to provide the configurable current with a predetermined current level.

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49. (New) The circuit of Claim 47 wherein the current source circuit comprises an IGBT and a resistor.

50. (New) The circuit of Claim 35 wherein the energy transfer circuit comprises four legs, each of the four legs of the energy transfer circuit including an IGBT switch circuit.

51. (New) The circuit of Claim 50 wherein the defibrillation and pacing pulses are selectably provided as a biphasic pulse or monophasic pulse.

52. (New) The circuit of Claim 51 wherein the defibrillation pulse is a biphasic pulse having a first phase with a first polarity and a second phase of a second polarity, and wherein the pacing pulse is a monophasic pulse with the second polarity.

53. (New) The circuit of Claim 51 wherein the defibrillation pulse is a biphasic pulse having a first phase with a first polarity and a second phase of a second polarity and the pacing pulse is a biphasic pulse having a first phase of the second polarity and a second phase of the first polarity.

54. (New) The circuit of Claim 50 wherein, during the pacing mode, the control circuit is configured to determine the predetermined level in charging the energy storage capacitor to achieve a predetermined current level for a subsequently provided pacing pulse.

55. (New) The circuit of Claim 50 wherein the H-bridge circuit comprises a current sense circuit coupled to the energy storage capacitor and the control circuit, and wherein the current sense circuit is configured to detect a current level of current provided by the energy storage capacitor when the circuit is providing a pacing pulse.

56. (New) The circuit of Claim 55 wherein, during the pacing mode, the control circuit is configured to cause the third and fourth legs of the energy transfer circuit to conduct a predetermined level of current when the circuit is providing a pacing pulse.

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57. (New) The circuit of Claim 47 wherein the current sense circuit comprises an amplifier, a transformer and a resistor.

58. (New) The circuit of Claim 35, wherein the energy transfer circuit is configured as an H-bridge.

59. (New) The circuit of Claim 35, wherein the energy storage capacitor is a single capacitor.

60. (New) A method of providing a defibrillation pulse or a pacing pulse to a patient from a single unit, the method comprising:

charging an energy storage capacitor;

during a defibrillation mode, transferring energy from the energy storage capacitor to the patient in an external defibrillation pulse; and

during a pacing mode, transferring energy from the energy storage capacitor to the patient in an external pacing pulse.

61. (New) The method of Claim 60 wherein an energy transfer circuit is used to transfer energy from the energy storage capacitor to the patient in both the defibrillation and pacing modes.

62. (New) The method of Claim 60 wherein the energy storage capacitor is charged to a predetermined level so that the pacing pulse has a current of a predetermined level.

63. (New) The method of Claim 60 wherein the pacing pulse is a biphasic pulse.

64. (New) An apparatus for providing to a patient a defibrillation pulse during a defibrillation mode and a pacing pulse during a pacing mode, the apparatus comprising:

an energy storage capacitor;

a charging circuit for charging the energy storage capacitor;

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switch means coupled to the energy storage capacitor for selectively transferring energy from the energy storage capacitor to the patient externally; and

control means for causing the switch means to transfer energy from the energy storage capacitor to the patient externally in a defibrillation pulse during the defibrillation mode, and for transferring energy from the energy storage capacitor to the patient externally in a pacing pulse during the pacing mode.

65. (New) The apparatus of Claim 64 wherein the switch means comprises an energy transfer circuit, wherein the energy transfer circuit is selectively configurable to transfer energy from the energy storage capacitor to the patient in both the defibrillation and pacing modes.

66. (New) The apparatus of Claim 64 wherein the control means is configured to cause the charging circuit to charge the energy storage capacitor to a predetermined level wherein the pacing pulse has a current of a predetermined level.

67. (New) The apparatus of Claim 64 wherein the pacing pulse is a biphasic pulse.

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